

Dr.Mahalingam College of Engineering and Technology
Pollachi-642003
Department of Electronics and Communication Engineering
Publication in National/International Journal &Book Publications
July 2017 to June 2018

Sl. No	Name of Faculty	Title of the Paper	Journal Details	Details	
1.	T.Piraisoodi M.WilljuiceIruthayar ajan K.Mohaideen Abdul Kadhar	Application of Single and Multi-objective Evolutionary Algorithms for optimal Nonlinear Controller Design in Boiler –Turbine System	International Journal of Fuzzy Systems, Published by Springer, Vol 20, No 3, pp 803–816 P-ISSN: 1562-2479,E-ISSN: 2199-3211. DOI:10.1007/s40815-017-0382-2	Year of Publication	March 2018
				Impact Factor	2.198
				Indexing	Science Citation Index, SCOPUS, Google Scholar,listed in UGC
				No.of Citations	-
2.	K Mohaideen Abdul Kadhar Subramanian Baskar	A stopping criterion for decomposition-based multi-objective evolutionary algorithms	Soft Computing Journal, Published by Springer, Vol 22, Issue 1, pp253-272 ISSN: 1432-7643 (Print) 1433-7479 (Online)	Year of Publication	January 2018
				Impact Factor	2.198
				Indexing	Science Citation Index, SCOPUS, Google Scholar,listed in UGC
				No. of Citations	5
3.	Selvaraj Tamilselvi, Subramanian Baskar L Anandapadmanaban K Mohaideen Abdul Kadhar PR Varshini	Chaos-assisted multiobjective evolutionary algorithm to the design of transformer	Soft Computing Journal, Published by Springer, Vol 21, Issue 19, pp5675-5692 ISSN: 1432-7643 (Print) 1433-7479(Online)	Year of Publication	October 2017
				Impact Factor	2.367
				Indexing	Science Citation Index, SCOPUS, Google Scholar
				No.of Citations	2

4.	T.Piraisoodi M.WilljuiceIruthayar ajan K.Mohaideen Abdul Kadhar	An Optimized Nonlinear Controller Design for Boiler–Turbine System Using Evolutionary Algorithms	IETE Journal of Research P-ISSN:0377-2063 E-ISSN:0974-780x PP.1-12 DOI:10.1080/03772063.2017.1360805	Year of Publication	August 2017 (online)
				Impact Factor	0.909
				Indexing	Science Citation Index, SCOPUS, listed in UGC
				No.of Citations	-
5.	K. N. Vijeyakumar S. Elango S. Kalaiselvi	VLSI Implementation of High Speed Energy- EfficientTruncated Multiplier	Journal of Circuits, Systems, and Computers Vol. 27, No. 5,pp. 1850077-1 - 15 P- ISSN: 0218-1266 E-ISSN:1793-6454 DOI: 10.1142/S0218126618500779	Year of Publication	August 2017
				Impact Factor	-
				Indexing	SCOPUS, Google Scholar
				No.of Citations	-
6.	M.AbarnaErmini J.Dhanasekar, V.K.Sudha	Memristor Emulator using MCP3208 AND Digital Potentiometer	ICTACT Journal on Microelectronics, Vol.3,No.4, pp.473-476 ,P-ISSN:2395-1680 DOI:10.21917/ijme.2017.0084	Year of Publication	January 2018
				Impact Factor	-
				Indexing	-
				No.of Citations	-
7.	Sakthi K Dhinesh K	PAPR Reduction Technique for universal Filter Multi Carrier	International Journal of Advanced Research in Engineering Science & Technology,E-ISSN:2393- 9877,p-ISSN:2394-2444,vol 5,No.3,pp.434-438.	Year of Publication	March 2018
				Impact Factor	-
				Indexing	-
				No.of Citations	-

Books/Book Chapters Published 2017-2018:

Sl. No.	Name of Faculty	Title of the Book/Book Chapters	Publisher's Name	Year of Publication
1.	R.Sudhakar S.Bharathi V.Gurunathan	Hybrid Intelligence for Image Analysis and Understanding, Chapter 14:Analysis of Hand vein Images Using Hybrid Techniques , DOI: 10.1002/9781119242963.ch14 ISBN: 978-1119242925	John Wiley& Sons Ltd pp.330 to 349	August 2017
2.	K.N.Vijeyakumar S Kalaiselvi B.Vinoth kumar	ASIC Implementation of Low Power FP-AU using Reversible Logic 53 pages ISBN : 978-6139587056	LAP Lambert Publishing Academy	April 3,2018
3.	B.Vinoth kumr K.N.Vijeyakumar K.Saranya	Single Precision Floating Point Multiplier 63 Pages ISBN: 978-3-96067-655-3	Anchor Publishers	July 2017